| Notice of Allowability | Application No. | Applicant(s) |
|---|---|---|
| | 10/665,920 | MILLER, EDWARD E. |
| | Examiner | Art Unit |
| | Hiep Nguyen | 2816 |
| The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED in or other appropriate community. This application is su | this application. If not included nication will be mailed in due course. THIS |
| 2. The allowed claim(s) is/are 20-23 and 25-31. | | |
| 3. \boxtimes The drawings filed on $\underline{09-18-03}$ are accepted by the Exam | iner. | |
| 4. | | |
| Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 6. ☐ Interview Sur Paper No./M 7. ☐ Examiner's A | rmal Patent Application (PTO-152) nmary (PTO-413), lail Date mendment/Comment tatement of Reasons for Allowance TUAN T. LAM PRIMARY EXAMINER |

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) Application/Control Number: 10/665,920

Art Unit: 2816

Reason for allowance

The following is an examiner's statement of reasons for allowance: in view of the amendment filed on 07-18-05, the prior art reference of records (US Pat. 4,258,272) fails to teach or fairly suggest a comparator comprising first and second current mirrors, first and second hysteresis transistors for supplying hysteresis currents to the current mirrors as called for in claim 20; a comparator comprising a summing stage wherein, the summing stage includes first and second current mirrors and a hysteresis feed-back stage coupled to the output terminal and operable to supply a hysteresis current to the summing stage as called for in claim 23; a comparator comprising a summing stage including first and second current mirrors coupled to an amplifier stage for summing the outputs of the amplifier and a hysteresis feedback stage coupled to the output terminal and operable to supply a hysteresis current to the summing stage as called for in claim 28. Therefore, claims 20-23 and 25-31 are presently allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hiep Nguyen whose telephone number is (571) 272-1752. The examiner can normally be reached on Monday to Friday from 7:30am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hiep Nguyen

07-29-05

TUANT. LAM

PRIMARY EXAMINER